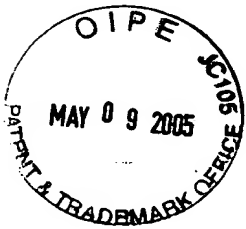


FIG. 1



CVD EXAMPLE: IDEAL AND MEASURED OTFs

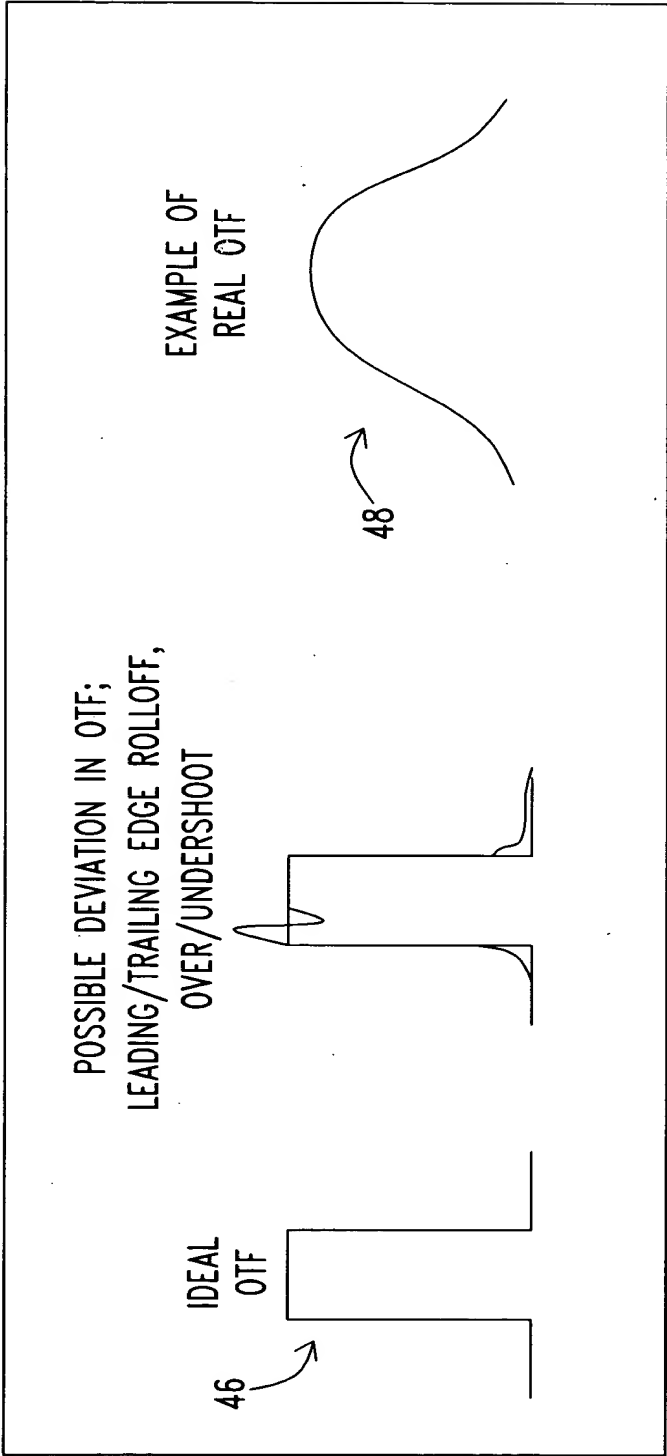


FIG. 2

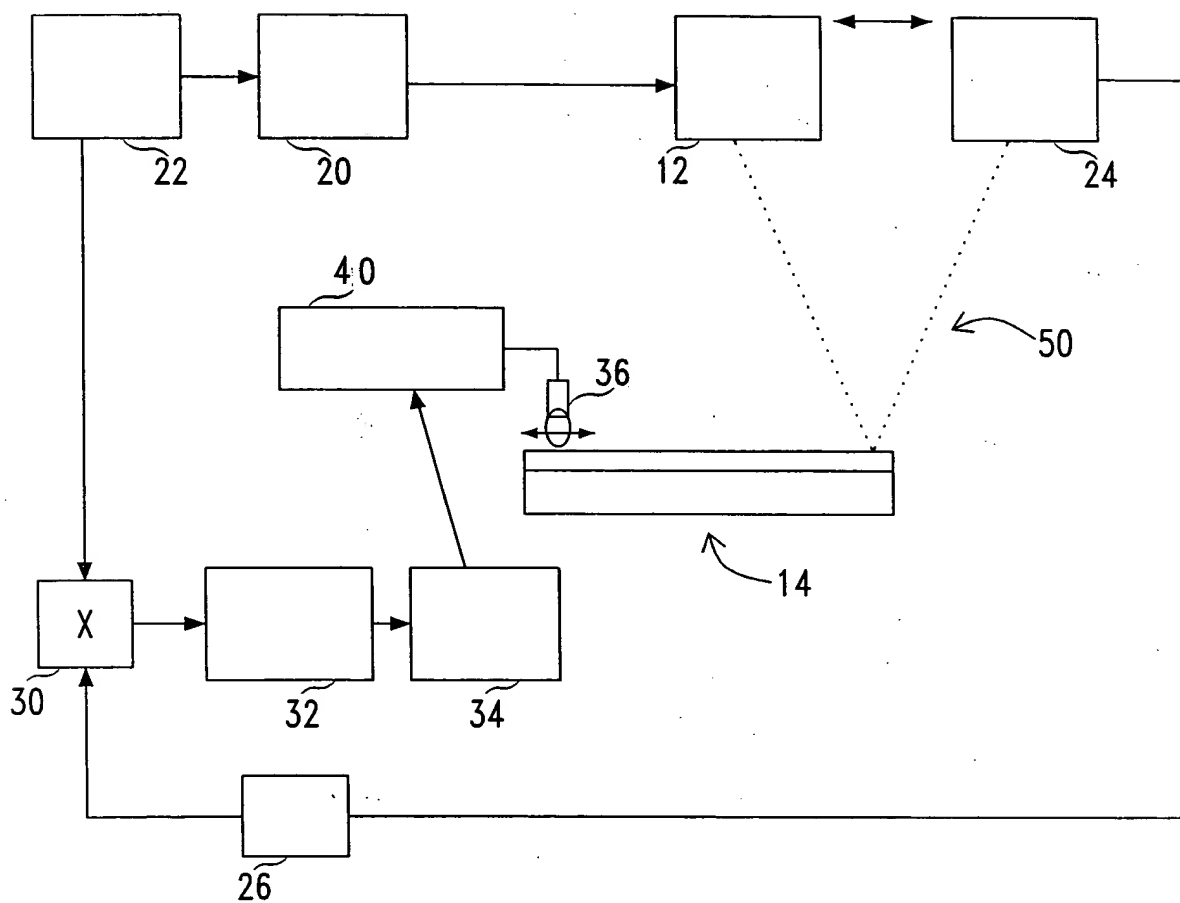


FIG. 3



DIELECTRIC ADD/DROP FILTER

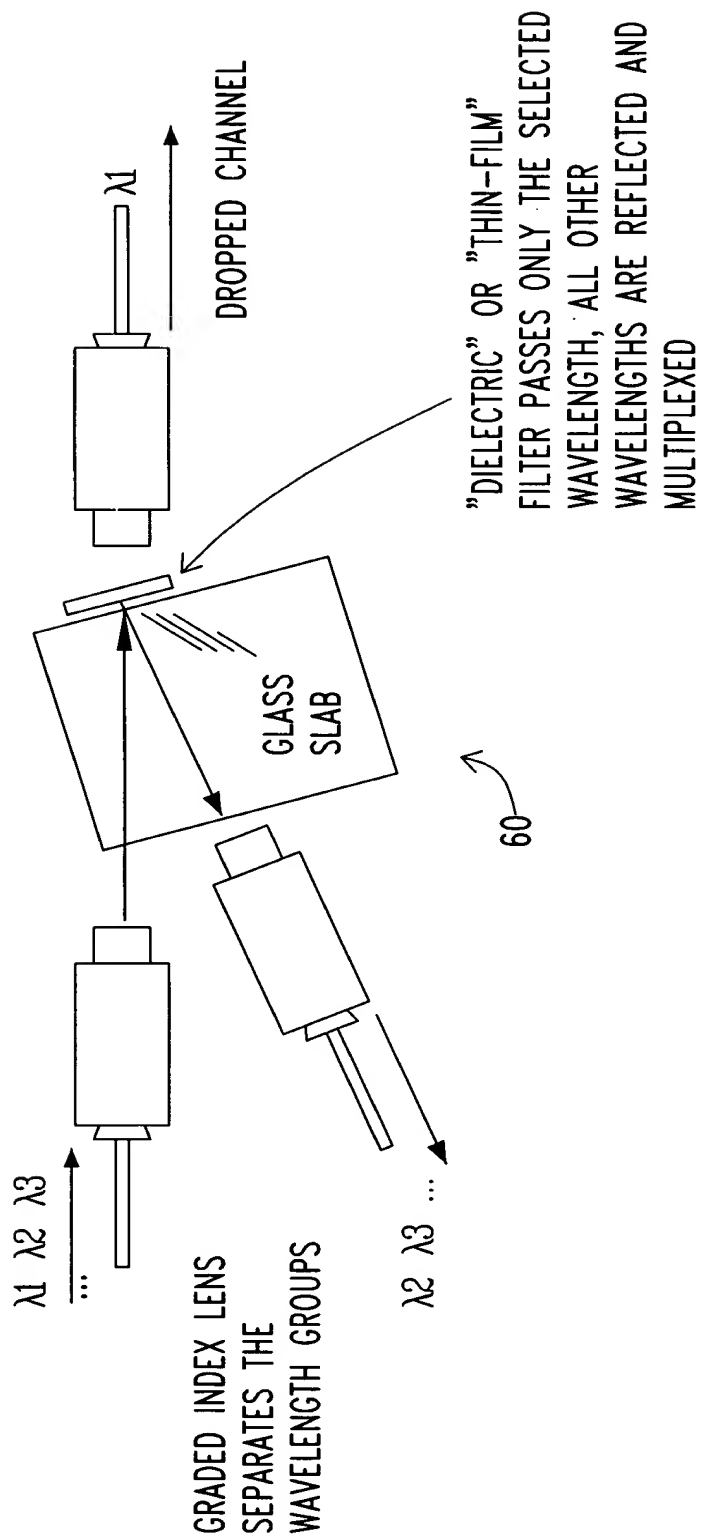


FIG. 4